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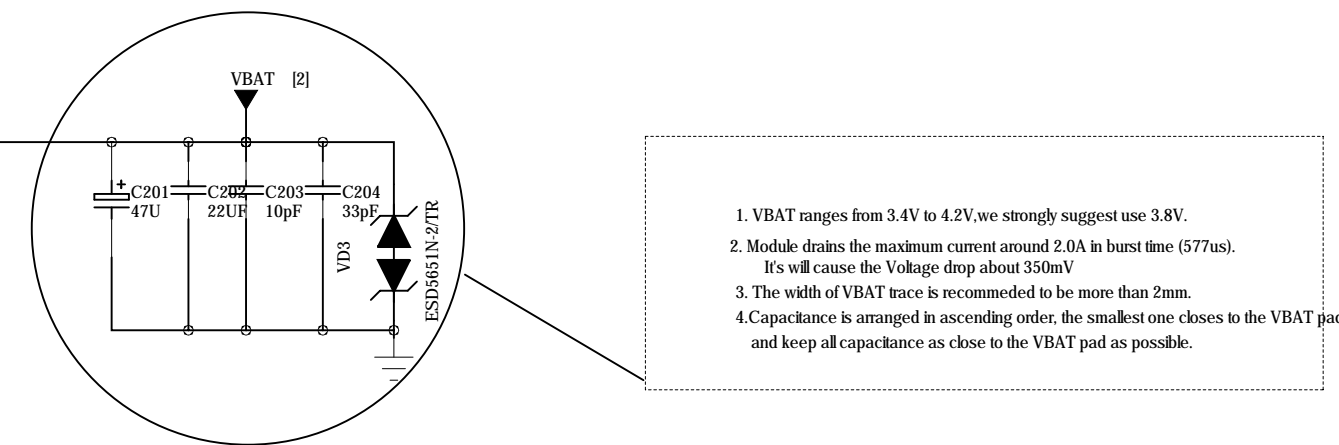
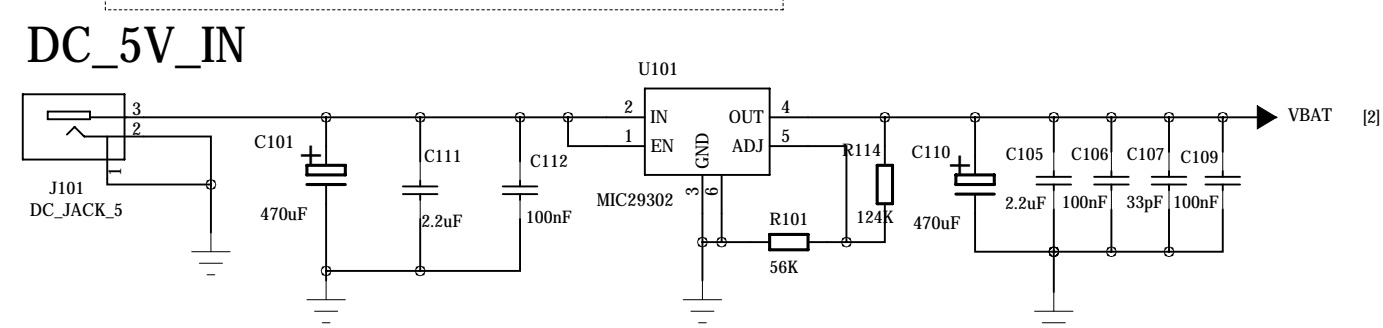
B

A

A

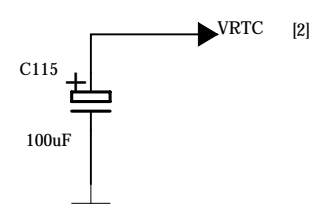
# L206 MODULE AND ACCESSORY

Note:  
The voltage converter should provide current at least 2.0A.  
It is used when the DC input voltage is below 7V.

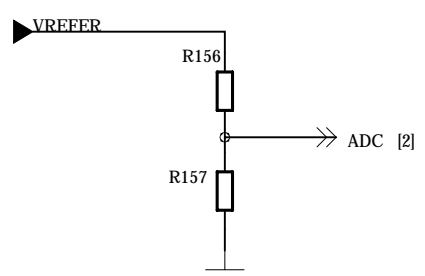
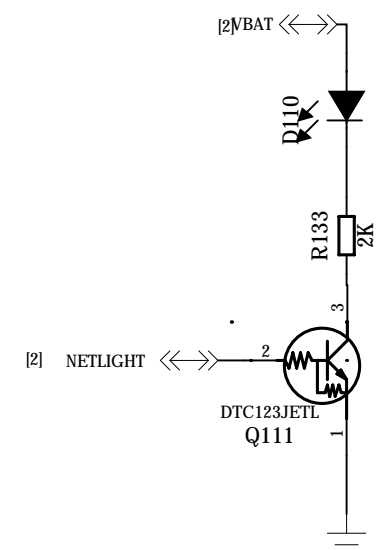
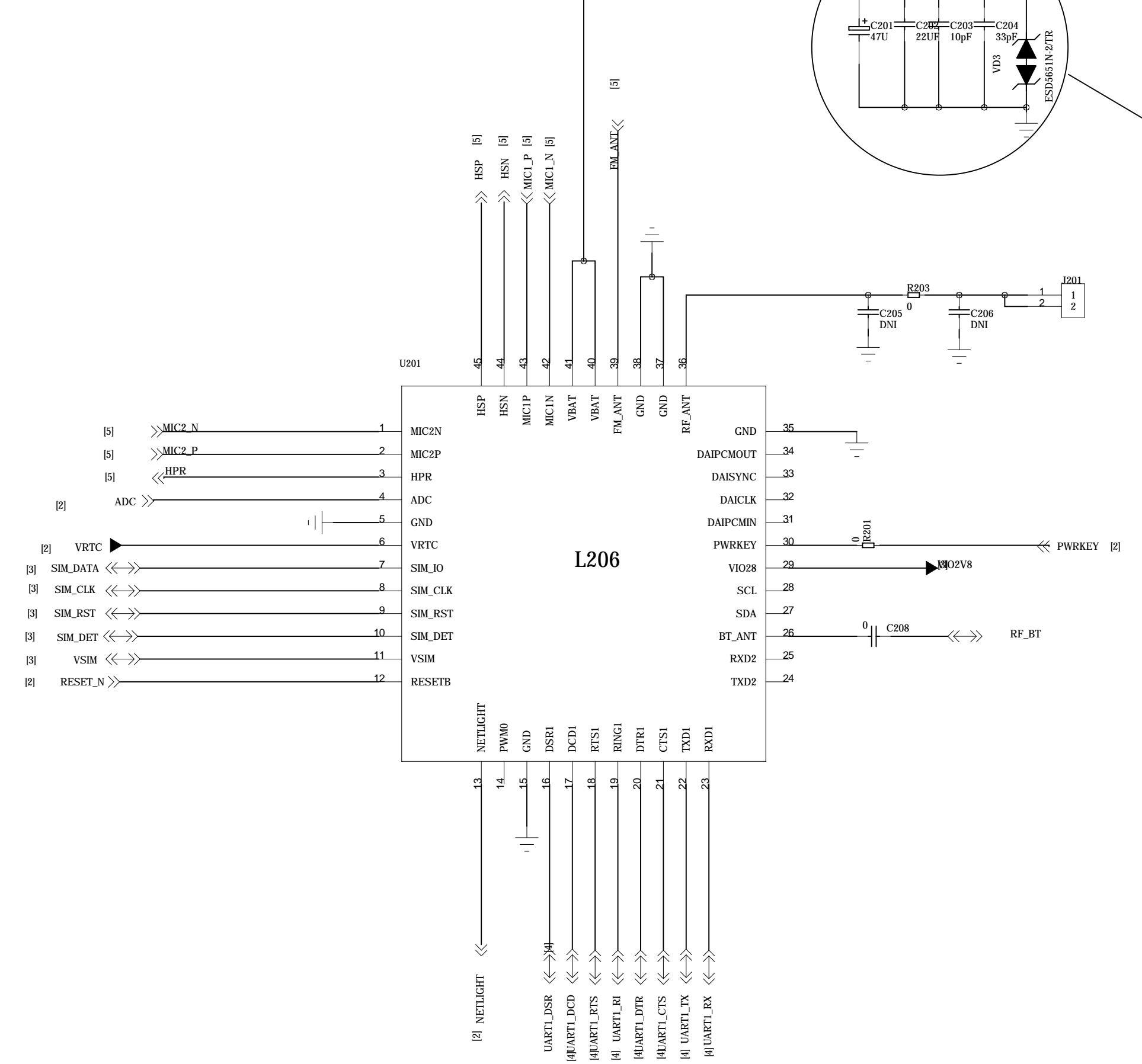


1. VBAT ranges from 3.4V to 4.2V, we strongly suggest use 3.8V.  
2. Module drains the maximum current around 2.0A in burst time (577us). It will cause the Voltage drop about 350mV.  
3. The width of VBAT trace is recommended to be more than 2mm.  
4. Capacitance is arranged in ascending order, the smallest one closes to the VBAT pad, and keep all capacitance as close to the VBAT pad as possible.

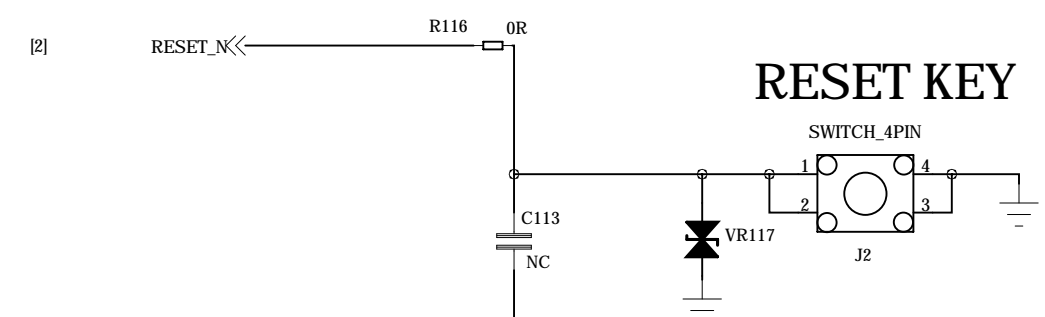
NOTE  
Coin cell battery could be changed to bulk cap for low cost solution



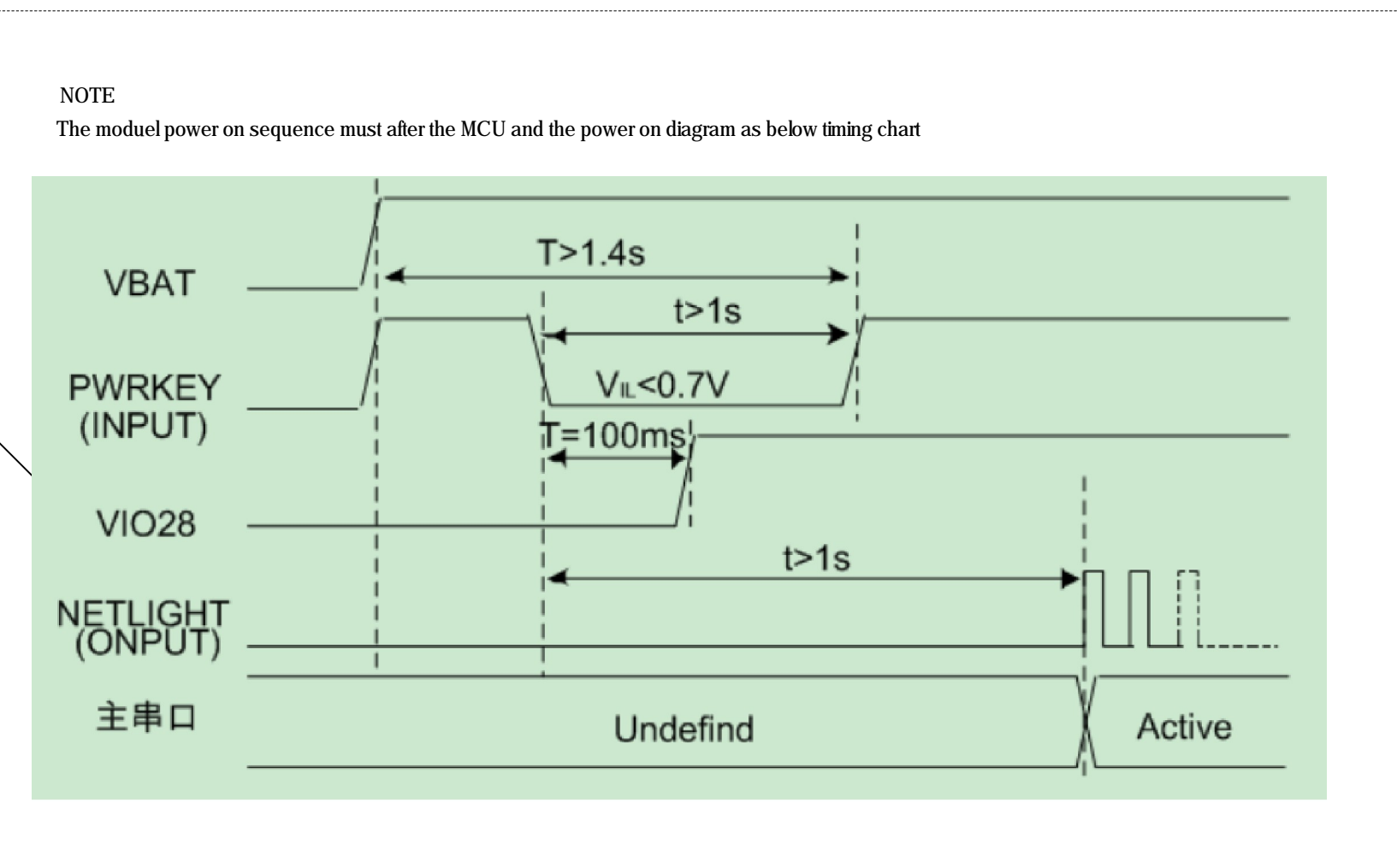
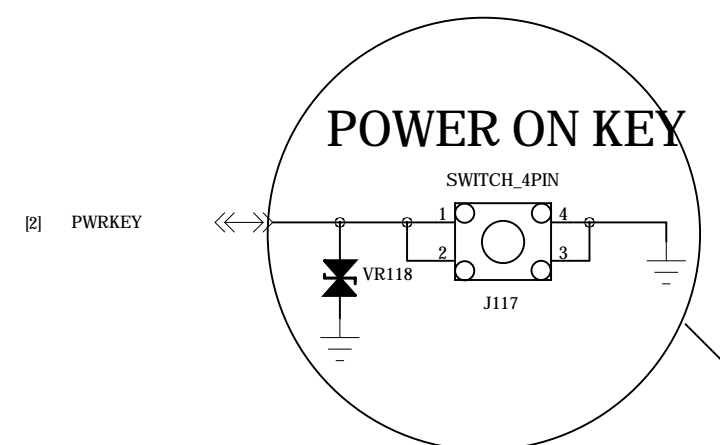
1. Our module have integrated a 1.5K ohm Current Limiting Resistance  
2. If use A 100uF Capacitor will keep 1minutes  
3. VRTC type voltage about 2.8V



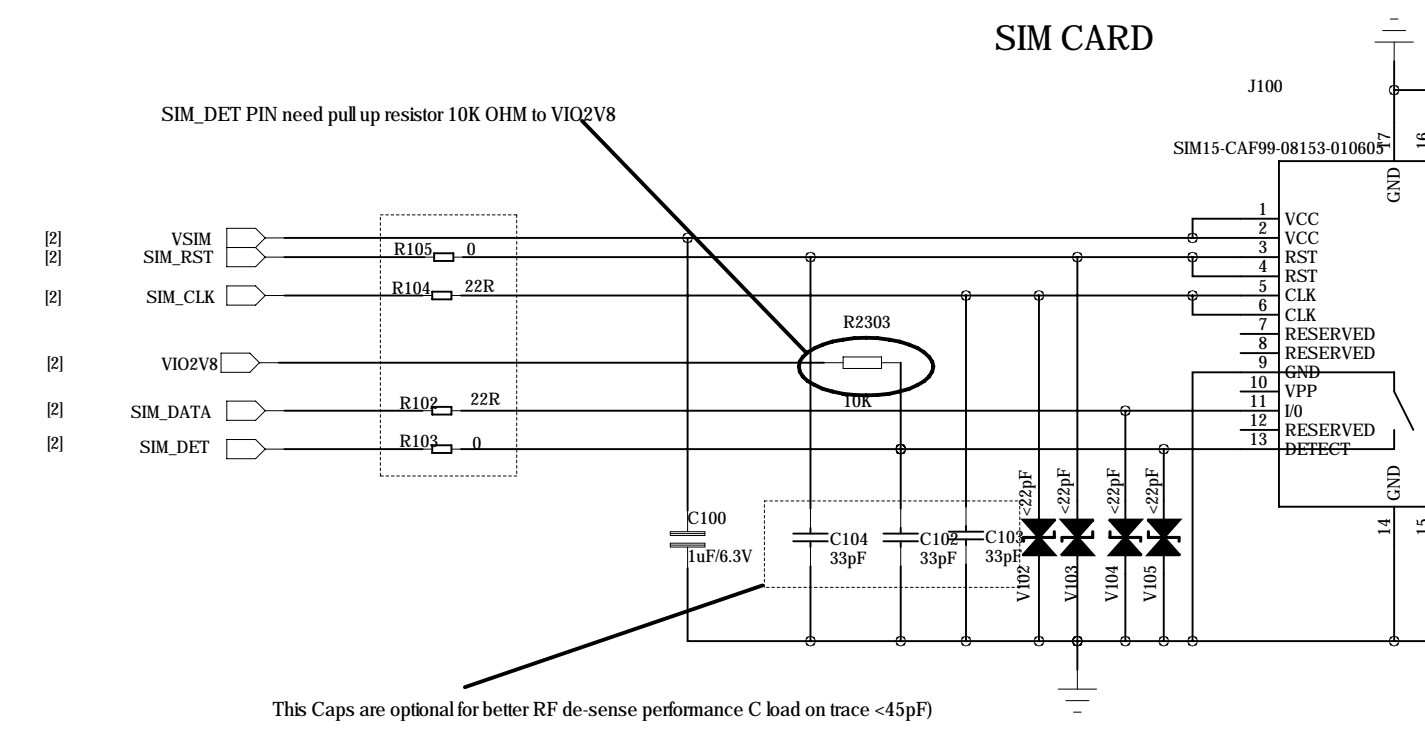
1. The voltage range of ADC input channel is 0 to 2.8V.  
2. Please select a high-precision divider resistance.



NOTE  
Reset active low input must keep above 105ms

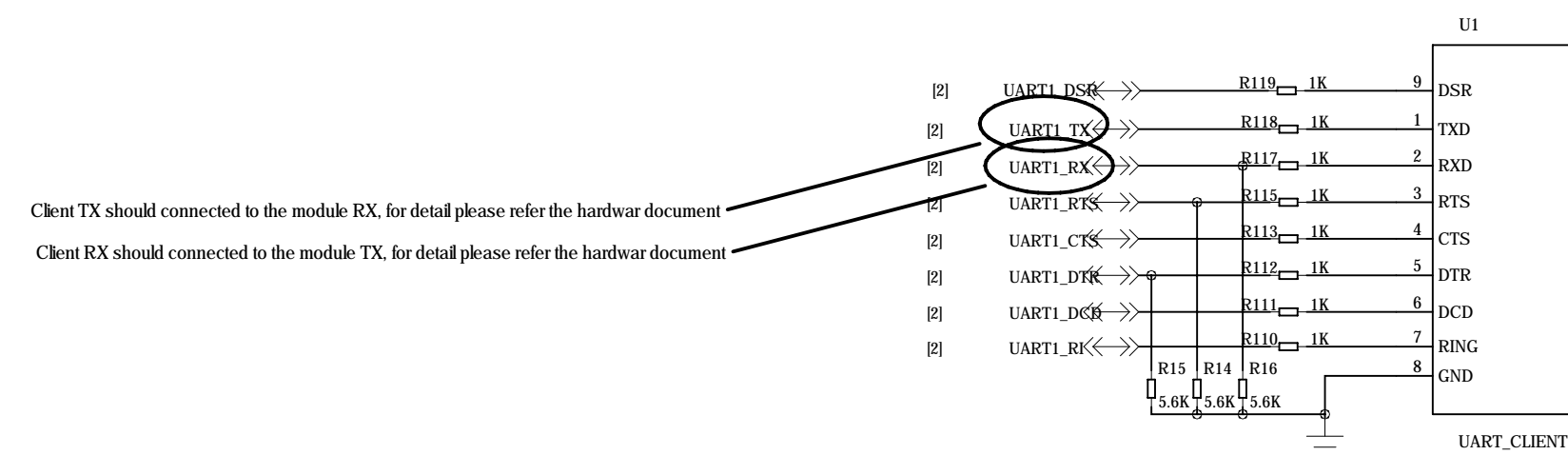


# SIM CARD



1. The value of C100 should be less than 1uF.
2. V102-V105 is used for protecting SIM card against ESD, and the junction capacitance should be less than 50pF. It should be placed nearby SIM card socket J100
3. Suggest add A 22 ohm serial resistor between the U201 and J100 for better suppression EMI and ESD
4. C102,C103,C104 is used to filter the RF interference
5. The SIM Socket(J100) integrated the SIM present detect pin. SIM\_DET need Add 10K Pull up resistor to V102/V8. If needn't the SIM insert detection you can let U201 PIN 10(SIM\_DET) float and NC the whole line.

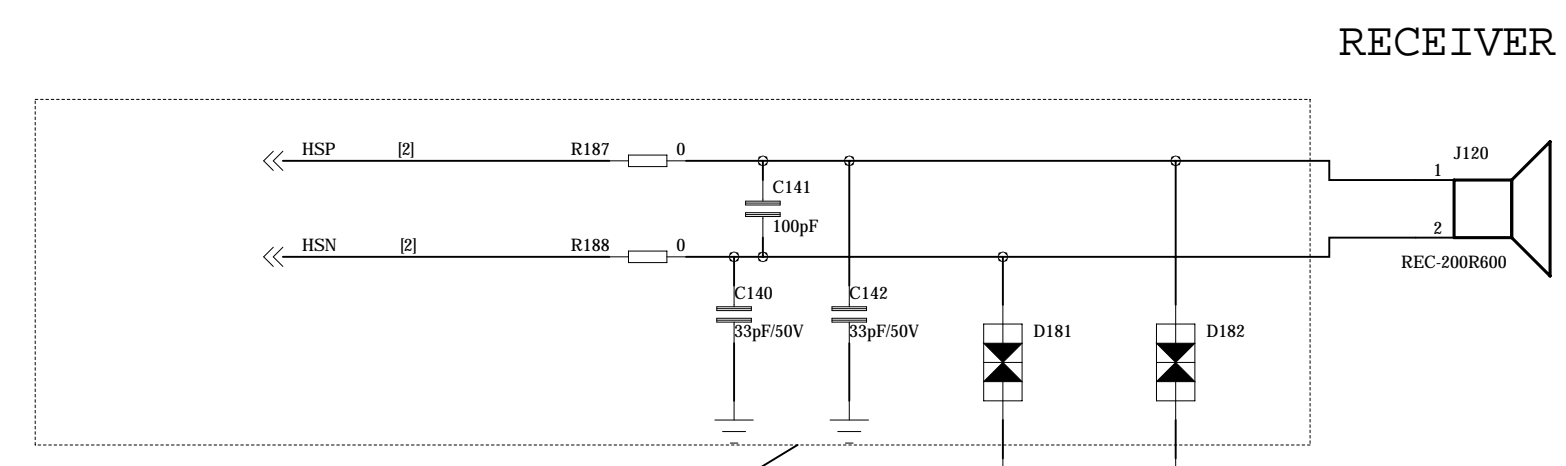
# FULL UART



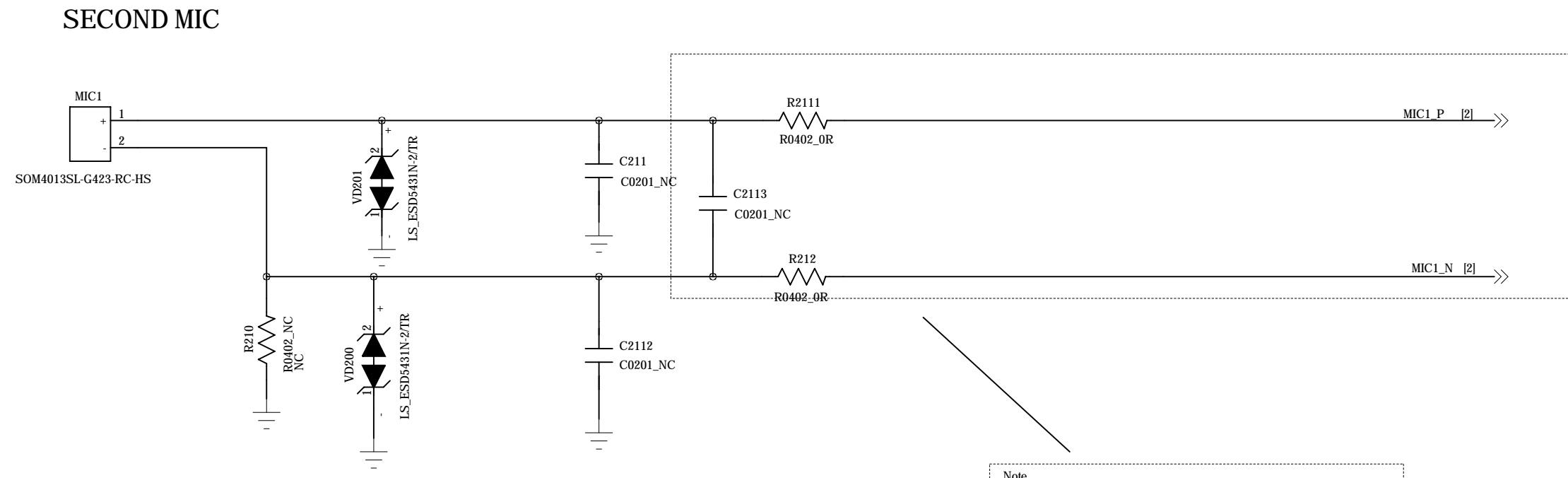
Connection of All Functional UART Port for 3.3V System

1. CTS/RTS will be used for HW flow control when mass data has been sent.
2. When AT+QSClk=1 is set on the module, customer's application can control the module to enter into or exit from the sleep mode through the pin DTR.
3. When DTR is set to high level, and there is no on-air or hardware interrupt, such as GPIO interrupt or data on serial port, the module will enter into sleep mode automatically.
4. It will output an indication signal when activity such as voice call or SMS is coming.
5. DCD is mainly applied in modem communication (PPP), the active status represents the communication link has been set up.
6. Please pay attention to the level match of UART in product application.

# AUDIO

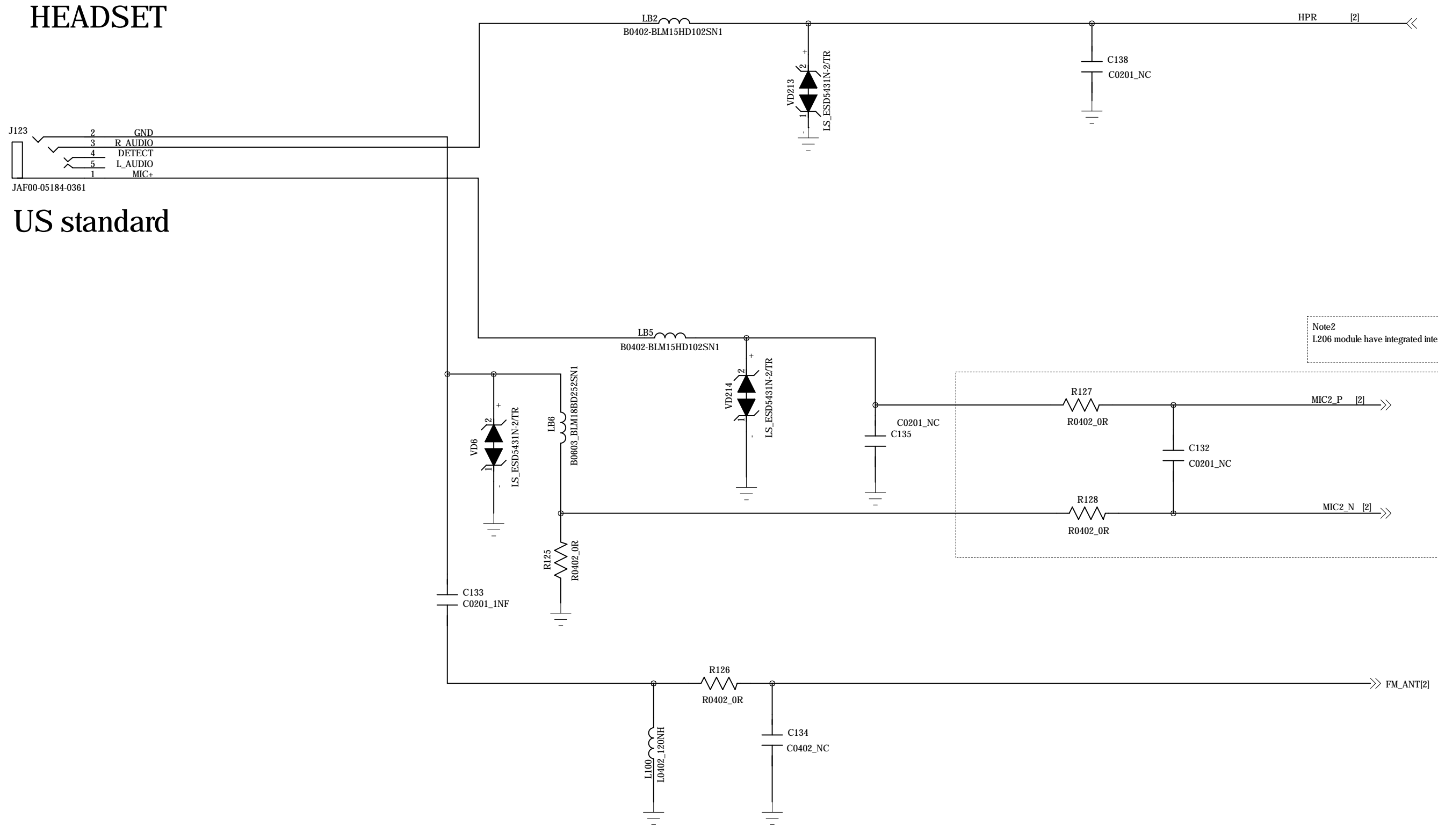
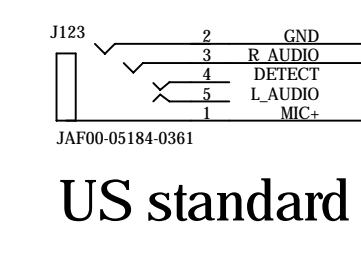


Note  
Route two traces as differential and the PCB line width at least route 0.45mm  
ESD placed close to receiver Jack



Note  
Route two traces as differential  
ESD closed to the MIC

## HEADSET



Note1  
Beads are needed to reduce noise  
HEADSET  
generated by audio/FM concurrency.

Note2  
L206 module have integrated internal pull up resistor for MICbias. So needn't MIC\_BIAS at MIC route

Differential route to the module